

Docket No.: MUH-12050

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MICHAEL JACOB ET AL.

Filed : CONCURRENTLY HEREWITH

Title : TEST CIRCUIT FOR AN ANALOG MEASUREMENT OF BIT
LINE SIGNALS OF FERROELECTRIC MEMORY CELLS

#57#DS
5/24/02
JCS87 U.S. PTO
10/053970
01/22/02

INFORMATION DISCLOSURE STATEMENT

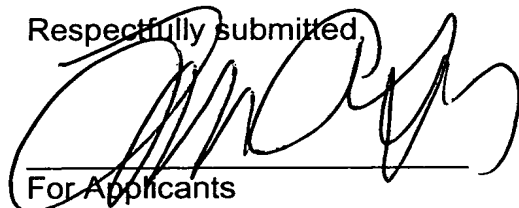
Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

OK
IN
In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications
are submitted herewith:

U.S. Patent 5,254,482 (Fisch), dated October 19, 1993;

Respectfully submitted,


For Applicants

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Date: January 22, 2002

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